

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)	
)	
S. Babar Raza)	Examiner: Hanh N. Nguyen
)	
Application No.: 10/773,948)	Group Art Unit: 2473
)	
Filed: February 6, 2004)	Confirmation No.: 2169
)	
For: Device that Provides the Functionality of)	
Dual-Ported Memory Using Single-)	Date: February 26, 2010
Ported Memory for Multiple Clock)	
Domains)	

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In compliance with the duty of disclosure under 37 CFR § 1.56 and in accordance with the practice under 37 CFR §§ 1.97 and 1.98, the Examiner's attention is directed to the documents listed on the enclosed PTO-1449.

In accordance with 37 CFR § 1.97(h), this Information Disclosure Statement is not to be construed as an admission that the information cited is or is considered to be material to patentability as defined in 37 CFR § 1.56(b), nor as an admission that the information constitutes prior art within the meaning of 35 USC §§ 102 and/or 103.

It is respectfully requested that the information listed on the PTO-1449 be considered by the Examiner, and that an initialed copy of the PTO-1449 be returned indicating that such information was considered.

No fee is believed necessary for the submission of this Information Disclosure Statement. However, The Commissioner is hereby authorized to charge any appropriate fees under 37 C.F.R. §§ 1.16, 1.17, 1.18, 1.20 and 1.21 that may be required to maintain pendency of the present application, and to credit any overpayments, to Deposit Account No. 50-3781.

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Should the Examiner have any questions, Applicants' undersigned attorney is reachable by telephone in our San Jose office at (408) 545-7194. The correspondence address of record appears below.

Respectfully submitted,

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